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Figure 1: Development Flow

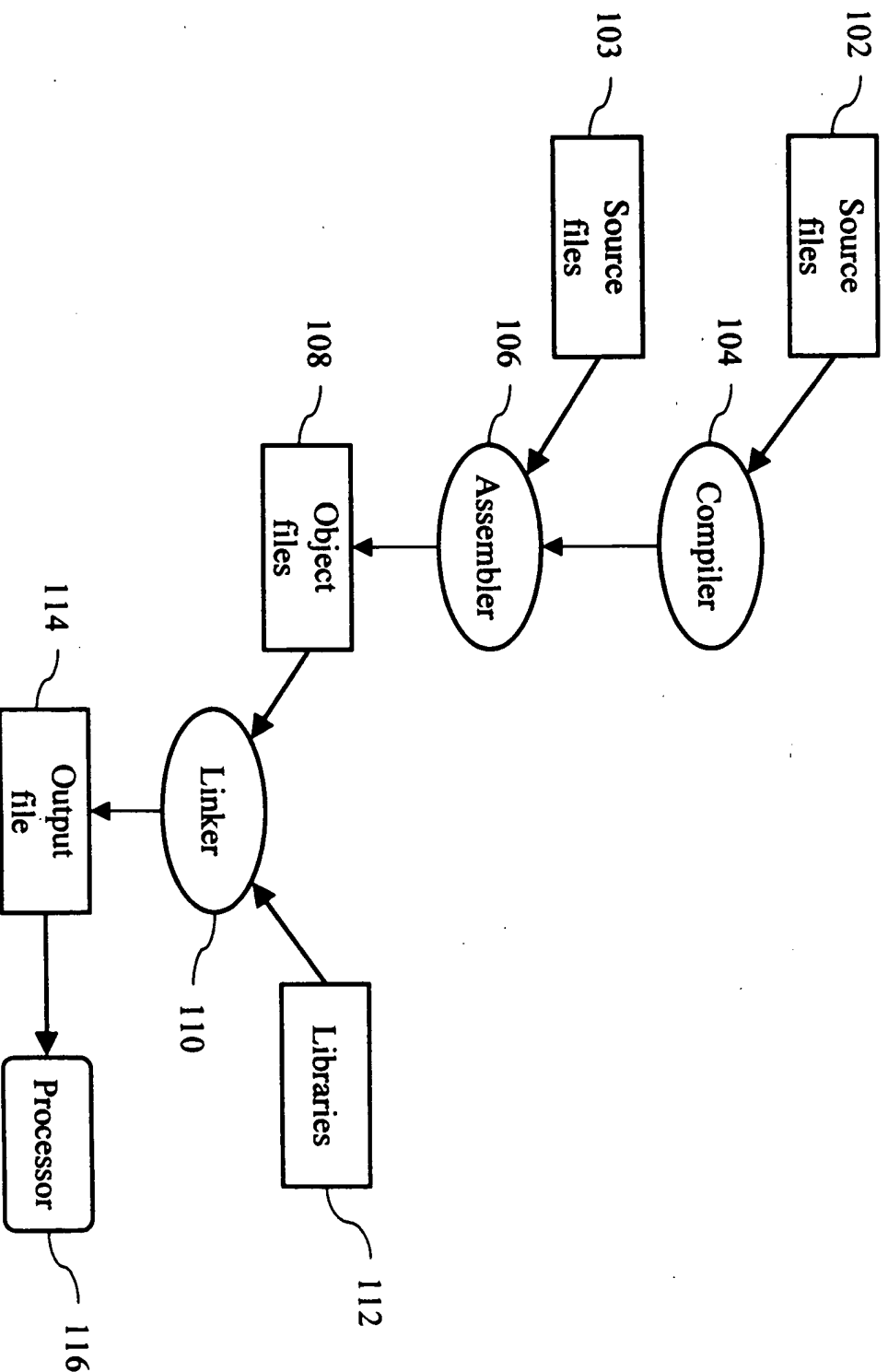


Figure 2A: Typical Linker Apparatus

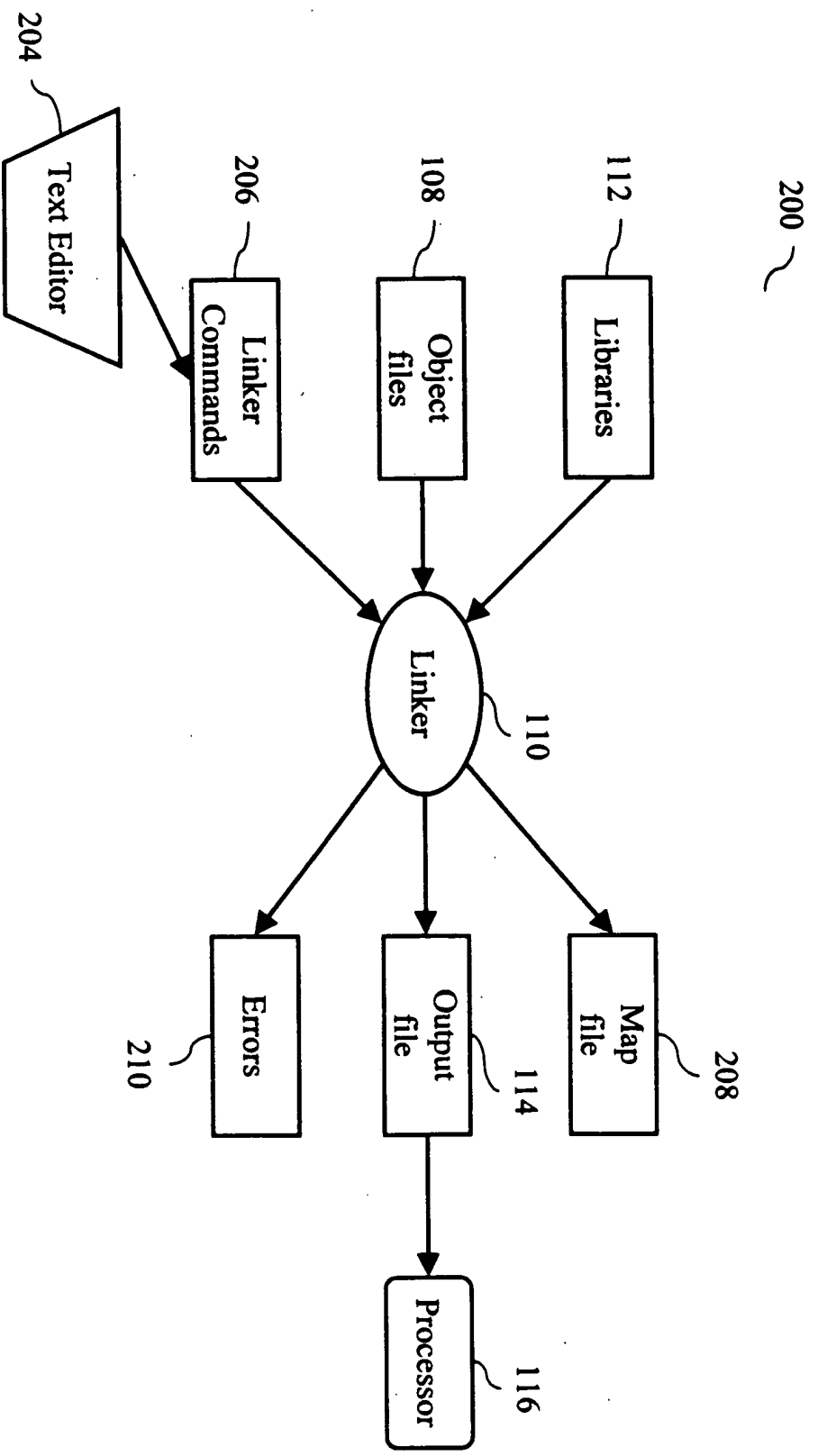


Figure 2B: Typical Linker Method

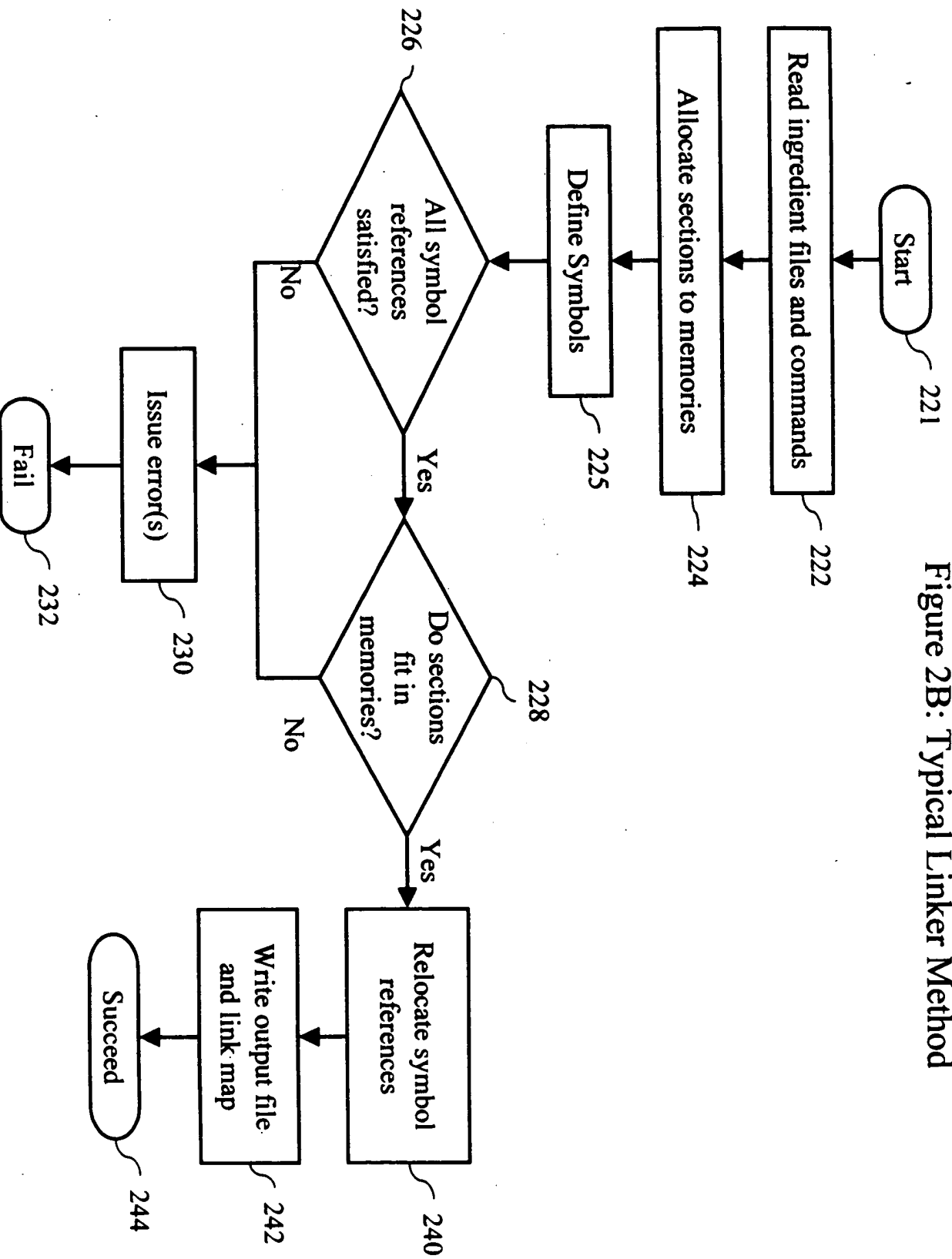


Figure 3: Linker Allocation and Output

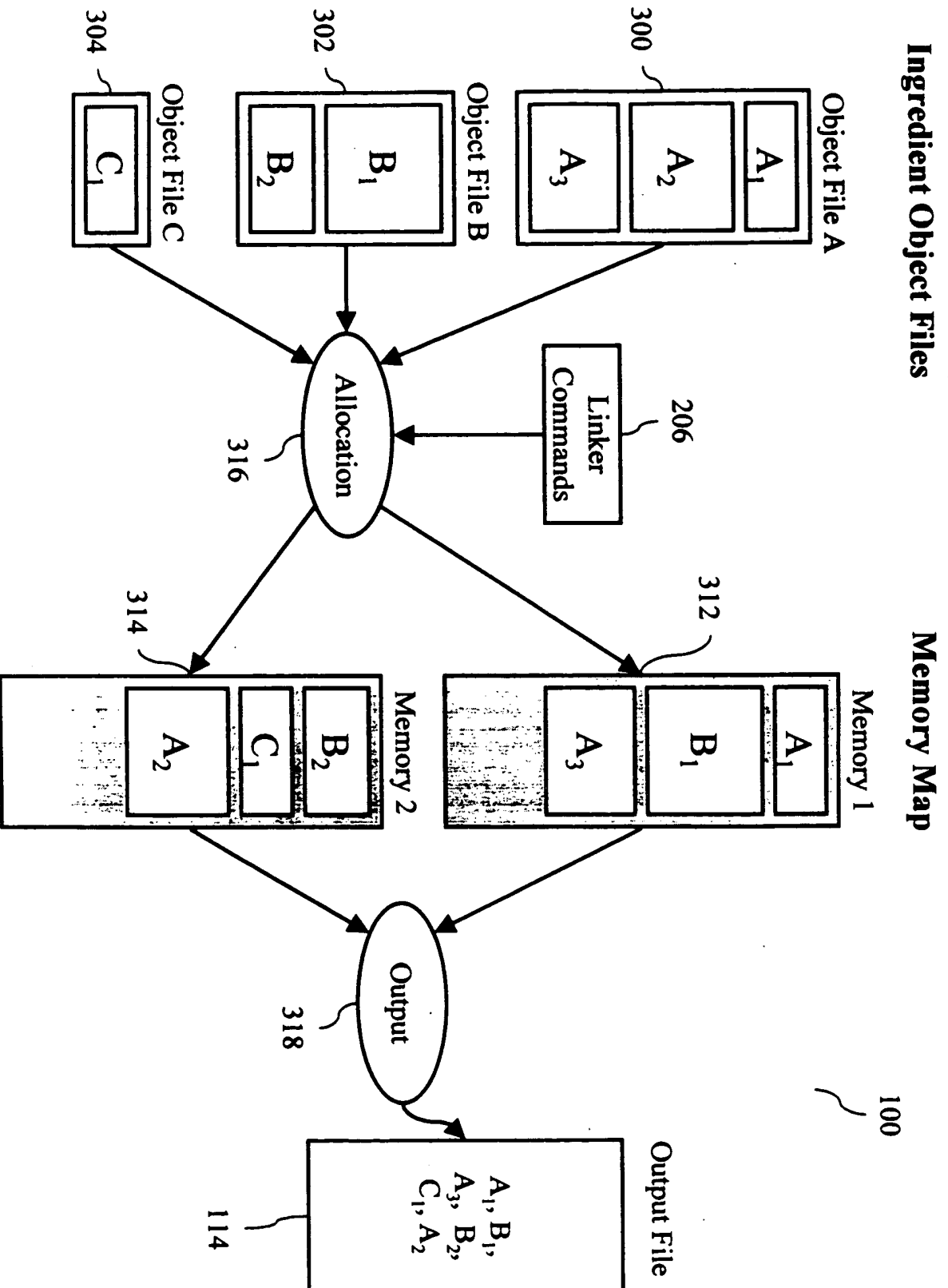


Figure 4: Linker Symbol Resolution

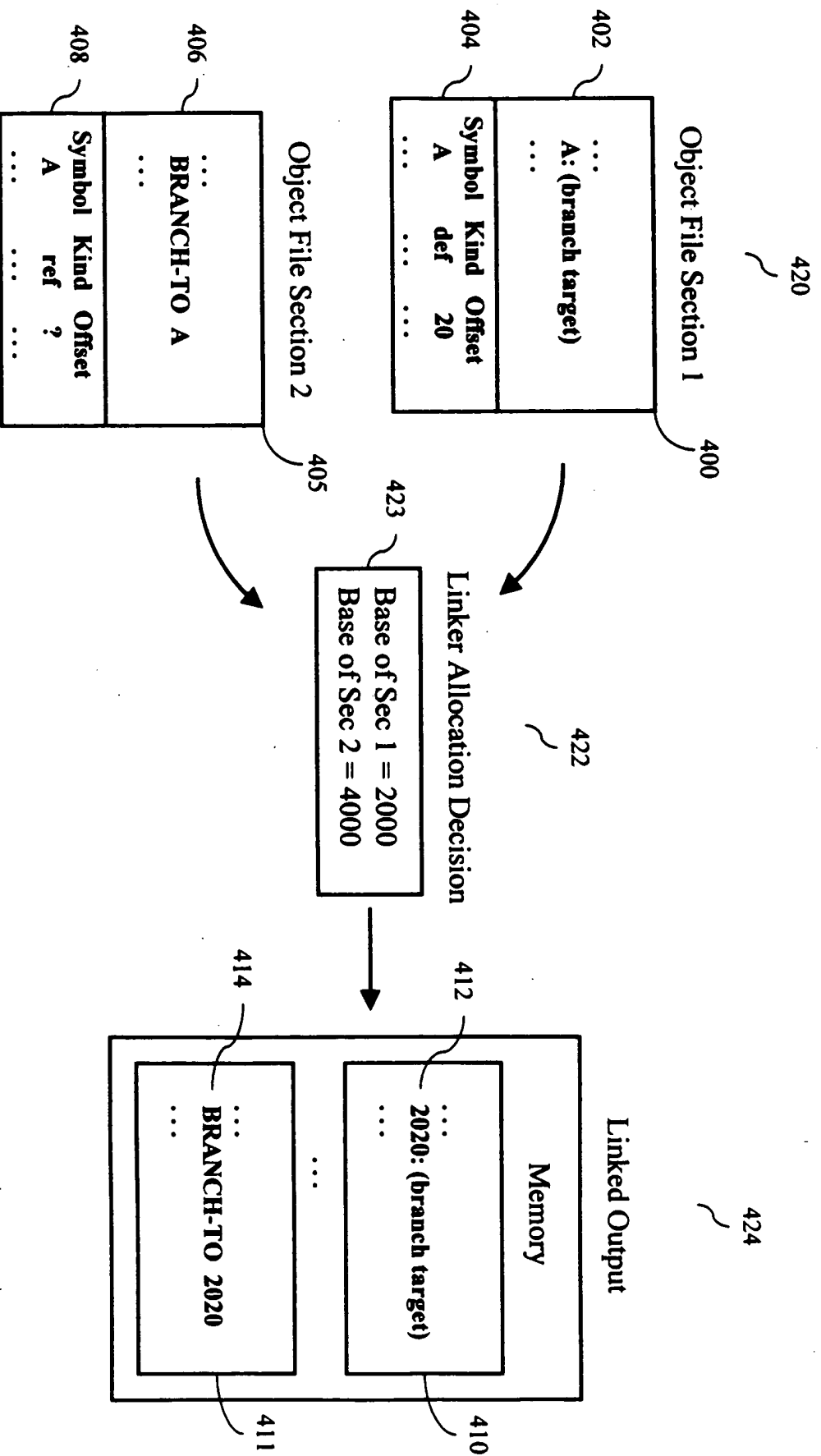


Figure 5A: Visual Linker Apparatus

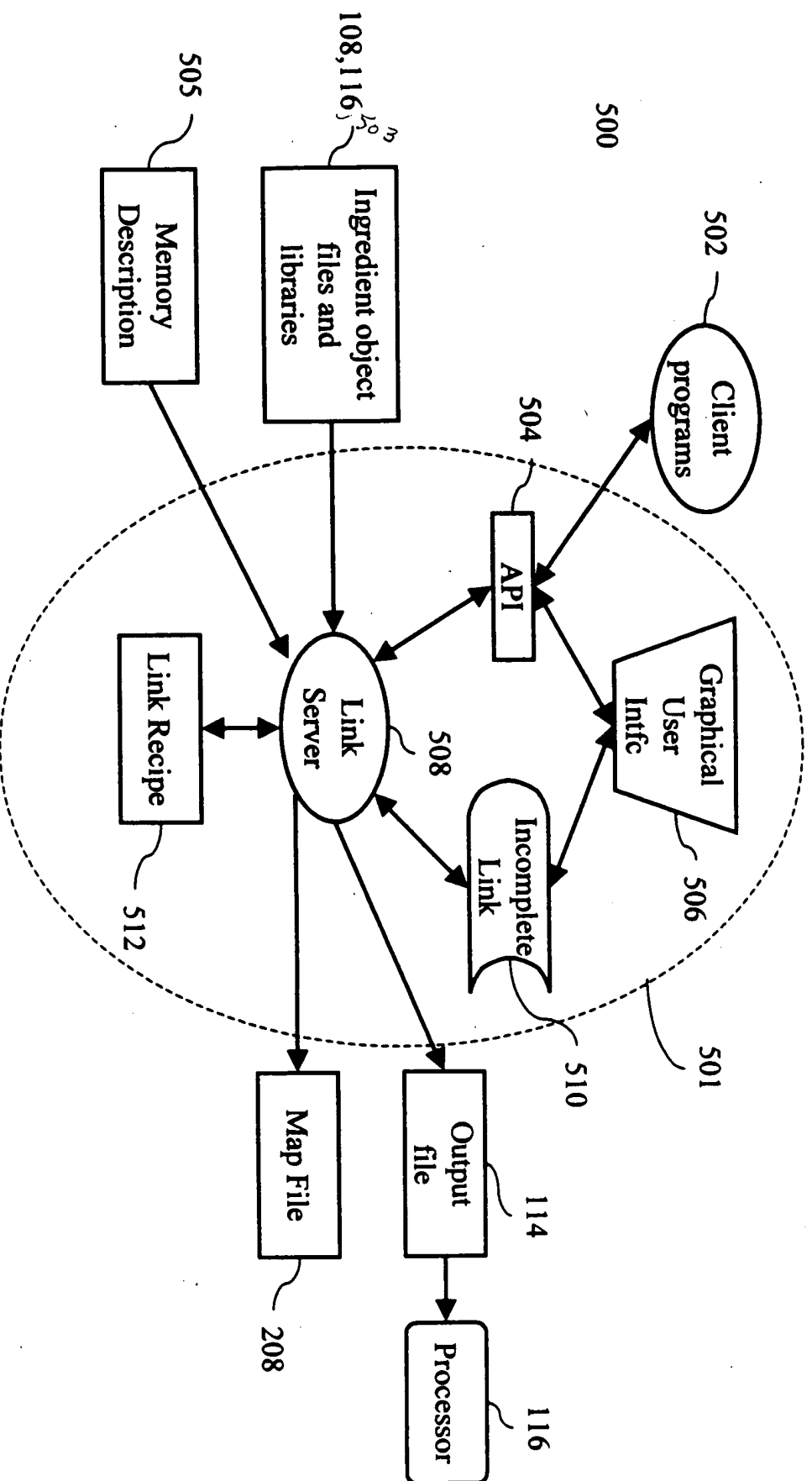
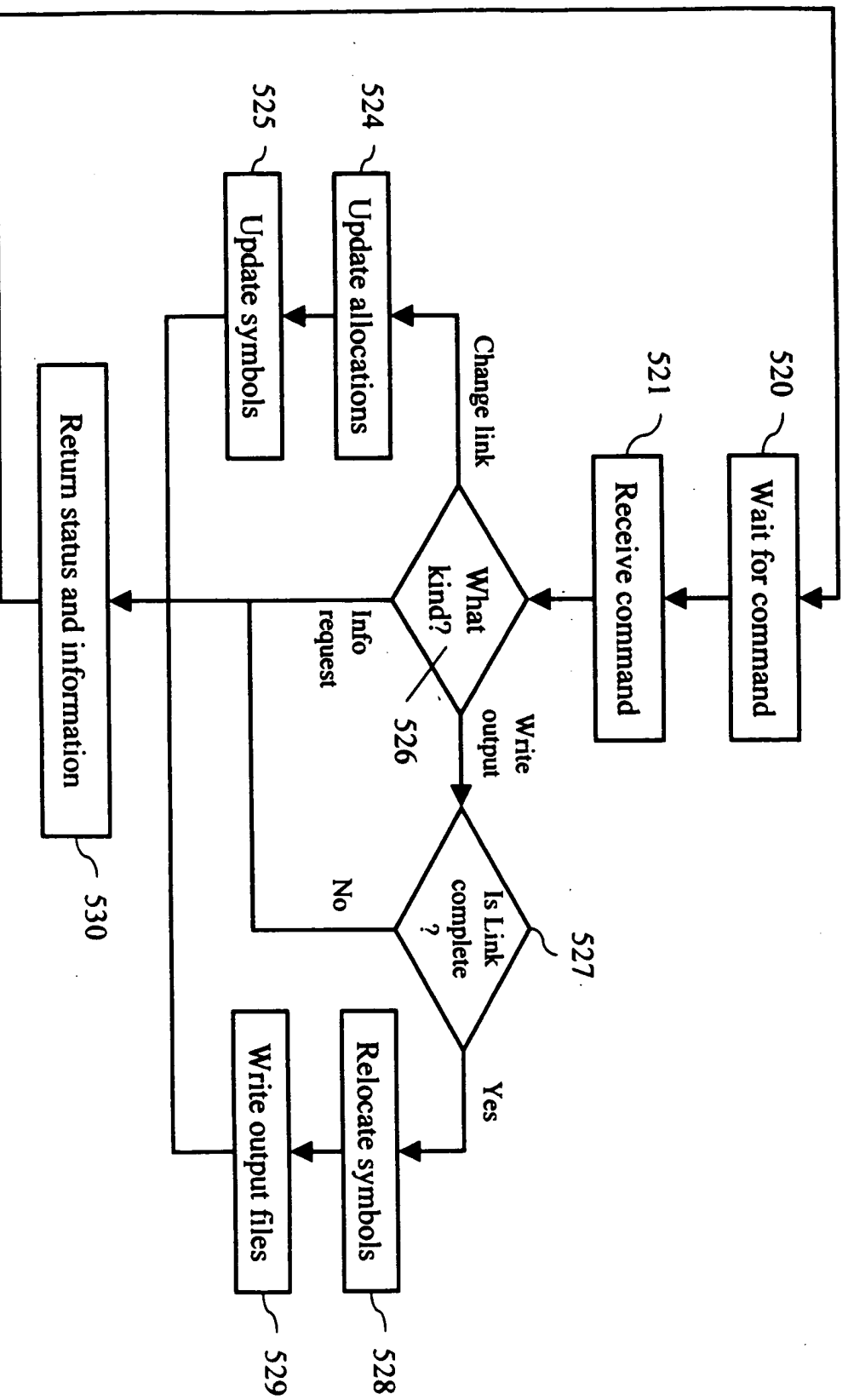


Figure 5B: Visual Linker Method

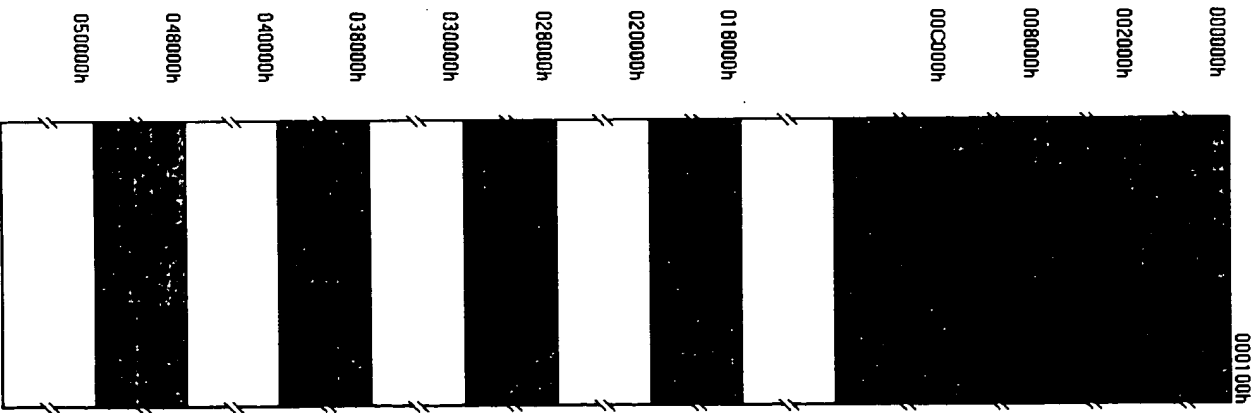


Bob

- Memory Maps
- EVMA320LCS49_o.vy1
- Link-Time Absolute Syms
- run_view
- Not Yet Placed
- Variables
- Constants
- data_memory
- data_memory_during_boot
- IO_memory
- IO_memory_during_boot
- program_memory
- RESERVED
- DARAM
- SARAM
- code_page_0
- code_page_0_MP1
- vectors
- code_page_1
- code_page_2
- code_page_3
- code_page_4
- code_page_5
- program_memory_when_MP0
- load_view
- Output Files
- All Input Files

ns\boot\text	Code	0000000h	0000444h	000045h
ns\text\text	Code	0000000h	000033Ch	000030Ch
maintst\text	Code	0000000h	000015h	000016h
tanhtst\text	Code	0000000h	0002B5h	0002B6h
comontst\text	Code	0000000h	000843h	000844h
ns\ldv\text	Code	0000000h	000036h	000037h
ns\l_neg\text	Code	0000000h	000004h	000005h
ns\l_err\text	Code	0000000h	000072h	000073h
ns\l_dv\text	Code	0000000h	000000h	000001h
ns\l_dv\text	Code	0000000h	000099h	00009Ah
ns\l_dv\text	Code	0000000h	00002Ah	00002Bh
ns\l_dv\text	Code	0000000h	00003Bh	00003Ch
ns\l_dv\text	Code	0000000h	00003Fh	000040h
ns\l_dv\text	Code	0000000h	00001Fh	000020h
ns\l_dv\text	Code	0000000h	0000A5h	0000A7h
ns\l_dv\text	Code	0000000h	000020h	000021h
ns\l_dv\text	Code	0000000h	000008h	000009h
ns\l_dv\text	Code	0000000h	00009Ah	00009Bh
ns\l_dv\text	Code	0000000h	00000Fh	000010h
ns\l_dv\text	Code	0000000h	00005Bh	00005Ch
ns\l_dv\text	Code	0000000h	00007Ch	00007Dh
ns\l_dv\text	Code	0000000h	00009Ch	00009Dh

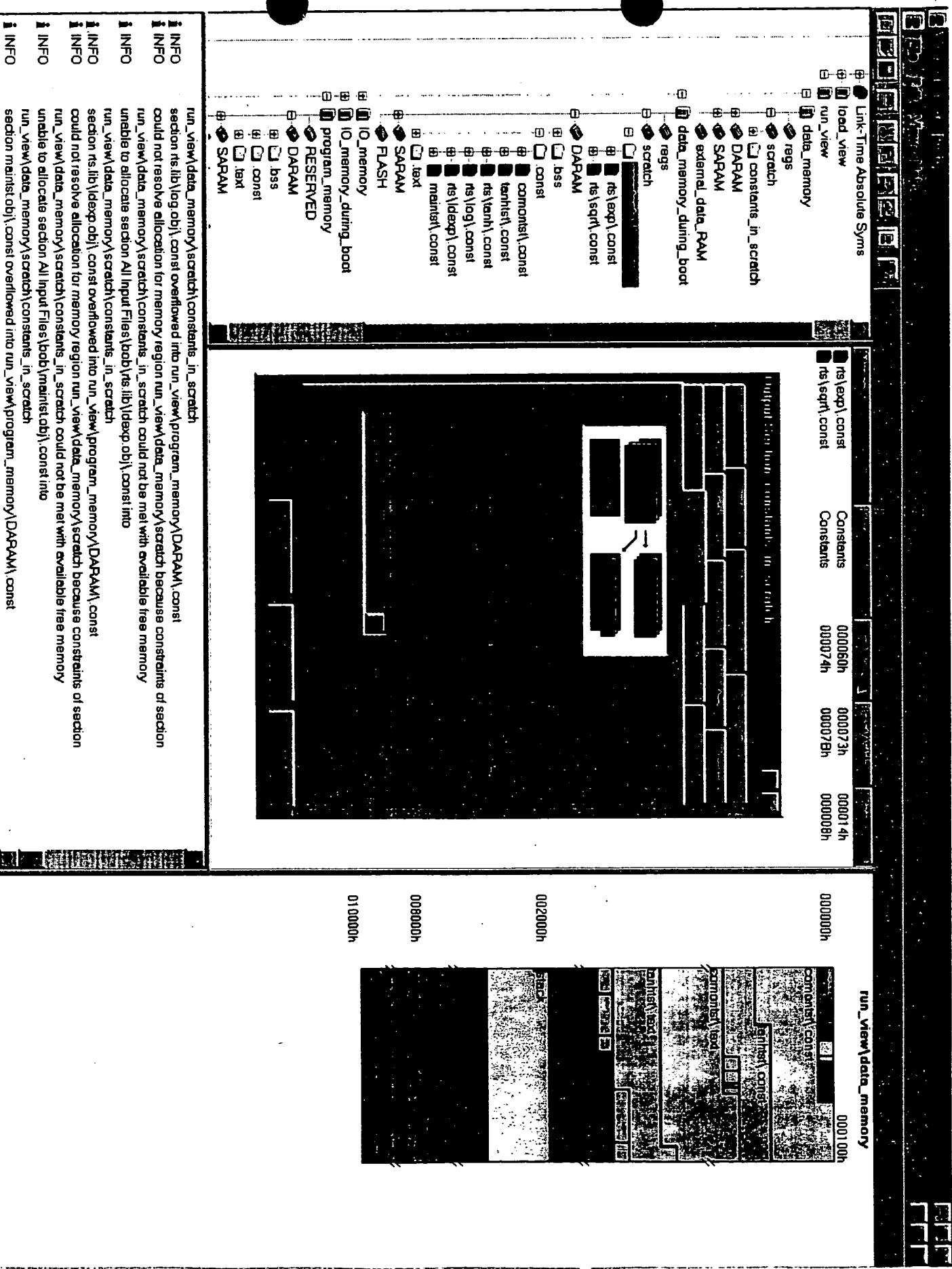
run_view\program_memory



- INFO begin parsing D:\src\src\bin\Debug\Templates\CS49\CS49\EVMA320LCS49_o.vy1.mem
- INFO done parsing D:\src\src\bin\Debug\Templates\CS49\CS49\EVMA320LCS49_o.vy1.mem
- INFO successfully completed parse of command line & files
- INFO concatenating all sections with base name: stack into a single output section of the same name
- INFO link-time symbol STACK_SIZE defined
- INFO concatenating all sections with base name: cinit into a single output section of the same name
- INFO link-time symbol cinit defined
- INFO link-time symbol cinit defined
- INFO link-time symbol print defined

Fig. 2

Fig. 8



run_view\data_memory\scratch\constants_in_scratch
section ns\log\obj\const overflowed into run_view\program_memory\DARAM\const
could not resolve allocation for memory region run_view\data_memory\scratch because constraints of section
run_view\data_memory\scratch\constants_in_scratch could not be met with available free memory
unable to allocate section All Input Files\obj\rs\lib\ldexp.obj\const into
run_view\data_memory\scratch\constants_in_scratch
section ns\lib\ldexp.obj\const overflowed into run_view\program_memory\DARAM\const
could not resolve allocation for memory region run_view\data_memory\scratch because constraints of section
run_view\data_memory\scratch\constants_in_scratch could not be met with available free memory
unable to allocate section All Input Files\obj\rs\lib\ldexp.obj\const into
run_view\data_memory\scratch\constants_in_scratch
section maint\obj\const overflowed into run_view\program_memory\DARAM\const

Fig. 9

Memory Maps

- Link-Time Absolute Syms
 - load_view
 - run_view
 - data_memory
 - regs
 - scratch
 - constants_in_scratch
 - DARAM
 - bss
 - const
 - SARAM
 - text
 - external_data_RAM
 - data_memory_during_boot
 - IO_memory
 - IO_memory_during_boot
 - program_memory
 - program_memory_when_MPU
- Output Files
- All Input Files

rs\boot\stack Variables 002BC3h 002BC3h 000000h

Output: too big to show

000000400h	000000400h	000000400h	000000400h	000000400h	000000400h	000000400h	000000400h	000000400h	000000400h
FFFFFFFFh	FFFFFFFFh	FFFFFFFFh	FFFFFFFFh	FFFFFFFFh	FFFFFFFFh	FFFFFFFFh	FFFFFFFFh	FFFFFFFFh	FFFFFFFFh

run_view\data_memory\scratch\constants_in_scratch could not be met with available free memory
unable to allocate section All Input Files\rs\lib\ldexp.obj\const into
run_view\data_memory\scratch\constants_in_scratch
section rs\lib\ldexp.obj\const overflowed into run_view\program_memory\DARAM\const
could not resolve allocation for memory region run_view\data_memory\scratch because constraints of section
run_view\data_memory\scratch\constants_in_scratch could not be met with available free memory
unable to allocate section All Input Files\rs\lib\ldexp.obj\const into
run_view\data_memory\scratch\constants_in_scratch
section maint.obj\const overflowed into run_view\program_memory\DARAM\const

text has been automatically added to load_view\Not Yet Placed
text has been automatically added to load_view\program_memory\SARAM

run_view\data_memory 000100h

0000000h

002000h

008000h

010000h

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100	